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HIGHLIGHTS

We examine a variety of perovskite solar cells using capacitance-based techniques

High-frequency capacitance signature is mainly attributed to the hole-transport layer

High-frequency capacitance is used to analyze dielectric constant of perovskite layers

Low-frequency capacitance is related to ionic movement in perovskite layers

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Influence of Charge Transport Layers on Capacitance Measured in Halide Perovskite Solar Cells

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SUMMARY

Capacitance-based techniques have been used to measure the electrical properties of halide perovskite solar cells (PSCs) such as defect activation energy and density, carrier concentration, and dielectric constant, which provide key information for evaluating the device performance. Here, we show that capacitancebased techniques cannot be used to reliably analyze the properties of defects in the perovskite layer or at its interface, because the high-frequency capacitance signature is due to the response of charge carriers in the hole-transport layer (HTL). For HTL-free PSCs, high-frequency capacitance can be considered as the geometric capacitance for analyzing the dielectric constant of the perovskite layer because there is no trapping and de-trapping of charge carriers in the perovskite layer. We further find that the low-frequency capacitance signature can be used to calculate the activation energy of the ionic conductivity of the perovskite layer, but the overlapping effects with charge transport materials must be avoided.

INTRODUCTION

Organic-inorganic halide perovskite solar cells (PSCs) have drawn intensive attention due to the low-cost fabrication process and the rapid rise of the record power conversion efficiency (PCE) from \sim 3% in 2009 to more than 25% in 2019.^{1,2} To further improve the PCE of PSCs, it is highly preferred to understand the electrical properties of the perovskite absorbers because the PCE is determined by the electrical properties of PSCs, such as defect activation energy and density, carrier concentration, and dielectric constant. Various techniques have been employed to study the defect states in PSCs, such as thermally stimulated current (TSC),³ transient absorption spectroscopy,⁴ low-frequency noise spectroscopy,⁵ temperature-dependent photoluminescence (PL) microscopy,⁶ deep-level transient spectroscopy,⁷ thermal admittance spectroscopy (TAS), $^{\rm 8}$ and capacitance-voltage (C-V) measurements. $^{\rm Y}$ Capacitance-based techniques, such as TAS and C-V, have been the choice of method for measuring electrical properties of semiconductor devices and have played important roles in the development of thin-film solar cell technologies. For TAS, a small AC modulation voltage (V_{AC}) is applied to trap and de-trap the charge carriers at certain frequencies and temperatures, enabling the extraction of their thermal ionization energy from the resultant capacitance signature.¹⁰

The most commonly reported capacitance-frequency-temperature (C-f-T) spectra, i.e., TAS spectroscopy, of PSCs in the literature typically show two capacitance

Context & Scale

Capacitance spectroscopy techniques have made significant contributions to the modern semiconductor research. By probing the alternating current response of semiconductor materials and devices with respect to bias-voltage and frequency at different temperatures, capacitance-based techniques provide crucial information, including defect activation energy and density, carrier concentration, and dielectric constant for evaluating and understanding the electrical properties of semiconductor materials. In recent years, capacitance-based techniques have been proliferated to metal halide perovskite solar cells. Although the fundamental principles of the characterization remain the same, specific challenges have been noted in the interpretation of measurement results. In this work, we show that capacitance-based techniques cannot be used to reliably analyze the properties of defects in the perovskite layer or at its interface due to the contribution of charge-transport materials.

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Figure 1. Thermal Admittance Spectroscopy Results of n-i-p, n-i, and n-p Devices (A–D) C- ω -T spectra obtained from (A) an n-i-p PSC with a 100-nm-thick spiro-OMeTAD HTL (device 1), (B) an n-i device without HTL (device 4), (C) an n-i-p PSC with a 10-nm-thick spiro-OMeTAD HTL (device 3), and (D) an n-p device without perovskite layer (device 5). These admittance measurements were performed in the dark at 0.0 V DC bias voltage and at temperatures ranging from 120 to 300 K with a 10 K temperature step size.

signatures,^{8,11–14} a high-frequency capacitance signature (<10⁵ Hz) and a low-frequency capacitance signature ($<10^3$ Hz), with an example shown in Figure 1A. The high-frequency capacitance signature is typically observed at temperatures below 240 K, which is labeled as D1 in Figure 1A. The low-frequency capacitance signature is typically observed at temperatures higher than 240 K, which is labeled as D2 in Figure 1A. The D1 signature has been often used to analyze defect activation energy and density of defects in perovskite absorber layers or at the interfaces in PSCs.^{11,12,15-23} The D2 signature has often been correlated with ion or electronic charge accumulation at an interface or with a slow transient-induced charge injection.²⁴ The activation energies of D1 ($E_a(D1)$) and D2 ($E_a(D2)$) reported in the literature are summarized in Table S1. The device architectures include both n-i-p PSCs, i.e., the electron-transport layer (ETL) (n type) is directly deposited on transparent conducting oxide (TCO)-coated glass substrate, and p-i-n PSCs, i.e., the hole-transport layer (HTL) (p type) is directly deposited on TCO-coated glass substrate. The temperature-dependent capacitance frequency (C-f-T) and temperature-dependent capacitance voltage (C-V-T) results have also been used to calculate the dielectric constant (ϵ_r) of the perovskite absorber, using the equation of $\epsilon_r = C_g t/A \epsilon_0$, where ϵ_0 is the vacuum permittivity and A the device area. C_g is the geometrical capacitance estimated with the assumption that the perovskite layer is fully depleted, i.e., the depletion width (W) equals to or is larger than the thickness (t) of the perovskite layer. In the literature, the reported dielectric constants of the perovskite absorbers depend on temperature, which has been attributed to phase transition.¹² The reported electrical dielectric constants of perovskites at two typical temperatures, i.e., low temperature (120–150 K) and high temperature (300 K) are also summarized in Table S1.^{11,23,25,26} The activation energies of D1 and D2 capacitance signatures and dielectric constants summarized in Table S1 show large discrepancies. Such differences have traditionally been attributed to the different electrical properties of perovskite absorbers synthesized by different processes and conditions.^{13,17,19,20} Therefore, the activation energy and defect density extracted from

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D1 capacitance signature have been used as guidance for improving the quality of PSCs. The large differences among the reported dielectric constants remain unclear.

In this paper, we show that the D1 high-frequency capacitance signature actually arises from the HTLs, not from perovskite absorbers. The D1 signature is not observed in PSCs without HTLs and is also observed in devices with HTL but without a perovskite layer. The high-frequency capacitance in PSCs without HTLs does not significantly depend on temperature and frequency, revealing no trapping and de-trapping of charge carriers. This suggests an apparent insulating behavior in the perovskite layer. Therefore, the high-frequency capacitance of PSCs without HTLs can be considered a geometric capacitance, from which the dielectric constant of the perovskite layer can be calculated. The discrepancy of the dielectric constants reported in the literature is due to the influence of HTLs. We further find that in n-i-p PSCs, the low-frequency capacitance spectra (<10³ Hz) cannot be described by discrete signatures, suggesting complicated coupling (physiochemical mixing and/or interaction) between the perovskite layer and ETL and/or HTL. However, in p-i-n PSCs, the ETL and HTL do not present coupling effects. In this case, the spectra can be described by a single signature, which is the so-called D2 signature reported in the literature. This signature allows the calculation of the activation energy of the ionic conductivity of the perovskite layer.

RESULTS AND DISCUSSION

Our conclusions were drawn based on the C-f-T spectra measured from a large number of devices in both n-i-p and p-i-n architectures and with various ETL and HTL materials. To exclude the influence of processing condition and composition of perovskite absorber, we fabricated the PSCs using perovskite absorber layers synthesized using identical perovskite precursor and processing conditions. TAS measurements were performed in dark using a 30 mV_{rms} perturbation sinusoidal AC voltage with a frequency sweep range of 10^{-1} – 10^{6} Hz and 0 V DC bias voltage, if not specifically mentioned. The temperature was varied in the range of 120-300 K with a 10 K temperature step size. The general trend of capacitance dependence on angular frequency ($\omega = 2\pi f$) and temperature (C- ω -T) of n-i-p PSCs using spiro-OMeTAD HTLs are consistent with those of n-i-p PSCs using spiro-OMeTAD HTLs reported in the literature (Table S1). Two peculiar capacitance signatures are observed in the spectra: one is the D1 signature observed at low temperature (120–220 K) and high frequency (ω < 10⁵ Hz), and the other one is the D2 signature observed at high temperature (T > 240 K) and low frequency (ω < 10^3 Hz). The activation energies of D1 and D2 and dielectric constants measured from our PSCs are summarized in Table 1.

Low-Temperature and High-Frequency Capacitance Signature (D1)

Planar n-i-p Devices

We first discuss the results measured from the n-i-p PSCs with and without spiro-OMeTAD HTL. The n-i-p PSCs with an HTL share the cell structure of FTO/SnO₂/ C₆₀-SAM/MA_{0.7}FA_{0.3}Pbl₃/spiro-OMeTAD/Au (devices 1–3). The device without spiro-OMeTAD HTL is named n-i (device 4). The device named n-p indicates the stacks without a perovskite layer, i.e., FTO/SnO₂/C₆₀-SAM/spiro-OMeTAD/Au (device 5). The SnO₂ ETLs were synthesized by atomic-layer deposition. The ultra-thin C₆₀-SAM layers and HTLs were deposited by spin coating. For the main results reported here, the perovskite layers have a composition of MA_{0.7}FA_{0.3}Pbl₃ and were deposited by one-step solution processing. More details can be found in the Supplemental Information. We chose this composition because it enables the fabrication of PSCs with PCEs > 21%.²⁷ PSCs using MAPbl₃ absorbers have also been measured to verify the generality of the conclusions.

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Device #	Devices Architecture	Activation Energy (eV)		Dielectric Constant	
		D1	D2	Low T (120 K)	High T (300 K)
1	FTO/SnO ₂ /C ₆₀ -SAM/ MA _{0.7} FA _{0.3} PbI ₃ /100 nm spiro-OMeTAD/Au (n-i-p)	0.166 ± 0.005	-	05.95	32.27
2	FTO/SnO ₂ /C ₆₀ -SAM/ MA _{0.7} FA _{0.3} PbI ₃ /50 nm spiro- OMeTAD /Au (n-i-p)	0.286 ± 0.027	-	14.56	30.89
3	FTO/SnO ₂ /C ₆₀ -SAM/ MA _{0.7} FA _{0.3} PbI ₃ /10 nm spiro- OMeTAD/Au (n-i-p)	-	-	31.71	35.92
4	FTO/SnO ₂ /C ₆₀ -SAM/ MA _{0.7} FA _{0.3} PbI ₃ /Au (n-i)	-	-	32.54	33.38
5	FTO/SnO ₂ /C ₆₀ -SAM/100 nm spiro-OMeTAD/Au (n-p)	0.172 ± 0.005	-	<u>02.28</u>	<u>09.89</u>
6	FTO/SnO ₂ /C ₆₀ -SAM/ MA _{0.7} FA _{0.3} PbI ₃ /spiro- OMeTAD*/Au (n-i-p)	0.286 ± 0.011	-	05.87	11.34
7	FTO/SnO ₂ /C ₆₀ -SAM/100 nm spiro-OMeTAD*/Au (n-p)	0.297 ± 0.003	-	<u>06.03</u>	07.02
8	FTO/MA _{0.7} FA _{0.3} PbI ₃ /Au (i)	-	-	29.89	29.61
9	FTO/SnO ₂ /MA _{0.7} FA _{0.3} Pbl ₃ / 100 nm spiro-OMeTAD/Au (n-i-p)	0.171 ± 0.002	-	05.95	33.11
10	FTO/C ₆₀ -SAM/ MA _{0.7} FA _{0.3} PbI ₃ /100 nm spiro-OMeTAD/Au (n-i-p)	0.161 ± 0.004	-	05.95	19.49
11	FTO/MA _{0.7} FA _{0.3} PbI ₃ / 100 nm spiro-OMeTAD/Au (i-p)	0.185 ± 0.004	-	05.95	12.89
12	ITO/PEDOT:PSS/ MA _{0.7} FA _{0.3} PbI ₃ /C ₆₀ /BCP/Ag (p-i-n)	0.019 ± 0.001	0.363 ± 0.012	34.58	26.12
13	ITO/PEDOT:PSS/C ₆₀ /BCP/ Ag (p-n)	0.016 ± 0.001	-	<u>02.69</u>	<u>02.01</u>
14	ITO/PTAA/MA _{0.7} FA _{0.3} PbI ₃ / C ₆₀ /BCP/Ag (p-i-n)	-	0.371 ± 0.009	34.02	37.24
15	ITO/PTAA/C60/BCP/Ag (p-n)	-	-	<u>01.18</u>	<u>01.32</u>
16	ITO/MA _{0.7} FA _{0.3} PbI ₃ /C ₆₀ / BCP/Ag (i-n)	-	0.365 ± 0.022	22.04	21.08

Table 1. Summary of Activation Energies of Capacitance Signatures and Dielectric Constants

List of the calculated activation energies of D1 and D2 capacitance signatures and electrical dielectric constants of our 16 devices with various architectures and ETLs/HTLs. FTO is fluorine-doped tin oxide (SnO₂) and C₆₀-SAM is substituted benzoic acid self-assembled monolayer. MA is methylamine, FA is formamidinium, spiro-OMeTAD is 2,2',7,7'-tetrakis(N,N-dip-methoxyphenylamine)-9,9'-spirobifluorene doped with Co-TFSI and Li-TFSI. Spiro-OMeTAD* indicates undoped spiro-OMeTAD. The polytriarylamines (PTAA) HTL is undoped. PEDOT:PSS is poly(3,4-ethylenedioxythiophene) polystyrene sulfonate. The BCP is bathocuproine. The perovskite layer is about 500 nm thick. "-" means that the $-\omega dC/d\omega$ versus ω spectra do not form clear peaks. Underlines indicate that the calculated parameters belong to the charge transport layer, not perovskite.

Figure 1A shows the C- ω -T spectra obtained from a typical n-i-p PSC with a 100-nmthick spiro-OMeTAD HTL doped with Li and Co salts (device 1). The high-frequency capacitance signature observed at temperatures from ~120 to ~220 K and high frequencies from 10¹ to 10⁵ Hz with capacitance ranging between 10⁻⁸ and 10⁻⁷ F/cm² is the D1 signature, which has been reported in the literature. This capacitance

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signature has been used to analyze defect properties in PSCs. Such analysis assumed a p-n junction in which the n-type semiconductor is the ETL, and the perovskite is the p-type semiconductor. The free electronic carriers diffuse and drift at the interface between the ETL and perovskite to form a space-charge region (SCR), which is the basis of TAS analysis.²⁸ If such a p-n junction indeed exists, similar signatures should be observed in devices without HTL. However, the C- ω -T spectra (Figure 1B) measured from our n-i (FTO/SnO₂/C₆₀-SAM/MA_{0.7}FA_{0.3}PbI₃/Au) perovskite device (device 4) show very different features, i.e., the D1 signature is no longer observed. These results strongly suggest that D1 signature is due not to the perovskite but in fact arises from the spiro-OMeTAD HTL. To confirm this conclusion, we measured the C- ω -T spectra from a PSC with only 10-nm-thick spiro-OMeTAD HTL (device 3). As shown in Figure 1C, the D1 signature is observed again but with significantly suppressed capacitance variation as compared with those measured from the PSC with 100-nm-thick HTL. It has been proposed that the increase of high-frequency capacitance was due to spiro-OMeTAD-induced increase of doping density in perovskite.²⁹ However, our results clearly suggest that the D1 capacitance signature is due to spiro-OMeTAD HTL. This is further confirmed by the C-ω-T spectra (Figure 1D) measured from a perovskite layer-free n-p device, i.e., FTO/SnO₂/C₆₀-SAM/spiro-OMeTAD/Au (device 5). The C-ω-T spectra at high frequencies are

very similar to those measured from the n-i-p PSC (Figure 1A), except for the small

differences of the capacitance values between the two cells.

It is important to note that for the device without spiro-OMeTAD HTL, the high-frequency capacitance (<10⁵ Hz) does not obviously depend on temperature and freguency as shown in Figure 1B. This indicates that there is no change to the depletion width or no trapping and de-trapping of charge carriers in the device without HTL. It implies that the perovskite layer behaves similar to an insulator or acts as an apparent insulator under high-frequency AC modulations. In a steady state, there is no electric field present inside perovskite layer, as observed by Kelvin probe force microscopy (KPFM) results measured in PSCs.^{30,31} KPFM measurements have shown junctions at perovskite-ETL and perovskite-HTL interfaces. Recent reports in the literature have shown that perovskite/ETL and perovskite/HTL interfaces are polarized due to ion migration.^{32–39} The interfacial polarization cancels the electric field induced by the ETL and HTL, showing no electric field inside perovskite layer. At short times (\leq ms) after a voltage perturbation, i.e., before the ions can transport and rearrange at the interfaces, only the capacitive charging occurs at the electrodes (see Scheme 1C in Belisle et al., 2017).³² The interfacial polarization-induced screening accounts for the apparent insulating behavior in perovskite layer observed in the C- ω -T spectra at high frequencies, which is equivalent to short times after voltage perturbations. The sharp decrease of capacitance at frequencies higher than 10^5 Hz (Figure 1B) is due to the effects of series resistance and inductance of the characterization system, 40 which leads to the frequency peak at 1.88 × 10⁵ Hz seen in the corresponding $-\omega dC/d\omega$ versus ω spectra (Figure S1). Similar peaks are seen in $-\omega dC/d\omega$ versus ω spectra of other devices. These frequency peaks are not discussed in this paper because they are not related to the properties of our devices. The apparent insulating behavior has two important implications. First, TAS cannot probe defects in perovskite bulk or at the interfaces between perovskite and charge transport layers (CTL). The D1 signatures reported in PSCs are due to HTLs. Second, the high-frequency capacitance can be used to calculate the dielectric constant of perovskite layer when PSCs without HTL are used for measurements. This is because under AC modulation, the capacitance at high frequency is due to geometrical contribution of the entire perovskite layer. Recently, Garcia-Belmonte and co-workers⁴¹ recognized the limitations of TAS analysis for characterizing the

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Figure 2. D1 Capacitance Signatures and Activation Energies

(A and B) $-\omega dC/d\omega$ versus ω of D1 signature extracted from the C- ω -T spectra shown in Figures 1A and 1D, respectively.

(C) Arrhenius plots, $ln(\omega_{peak}/T^2)$ versus $1/k_BT$, extracted from the $-\omega dC/d\omega$ versus ω spectra shown in Figures 2A and 2B, respectively.

(D) The calculated bias-dependent activation energies of the n-i-p PSC calculated from C- ω -T spectra measured with various DC bias voltages ranging from -0.4 to +1.0 V with a 0.1 V voltage step size.

high- and low-frequency capacitance signatures from the perovskite in PSCs. They pointed out that the assumption of intrinsic perovskite layer could obstruct the basic principle of depletion layer modulation in the perovskite layer. Our results show that the apparent insulating behavior of perovskite is the origin of the limitations.

Capacitance of spiro-OMeTAD has been reported in the literature.^{42–45} The origin of this capacitance was interpreted as chemical capacitance, which is defined as $C = q^2(\partial p / \partial \mu_p)$, where p is the hole density and μ_p is the electrochemical potential of spiro-OMeTAD.⁸ Here, we consider the traditional definition of capacitance for the spiro-OMeTAD HTL, i.e., C = dQ/dV, where Q is the stored charge and V is the applied bias voltage. This suggests that the D1 capacitance signature (Figure 1D) should represent the trapping and de-trapping of charge carriers in spiro-OMeTAD HTL doped with Li and Co salts. The C- ω -T spectra and their corresponding $-\omega dC/d\omega$ versus ω spectra of an n-p device using undoped spiro-OMeTAD HTL (device 7) are shown in Figures S2A and S2B, respectively. No D1 signature was observed, further confirming that the D1 signature can be used to calculate the activation energy of dopants in spiro-OMeTAD HTL by plotting the derivative of capacitance spectra in the form of $-\omega dC/d\omega$ as a function of ω , Figures 2A and 2B. The activation energy (E_a) is calculated using the following equation⁴⁶:

$$E_{a} = k_{B}T \ln(\nu_{th}\sigma_{p}N_{V} / \omega_{peak})$$
 (Equation 1)

where k_B is Boltzmann constant, T the absolute temperature, v_{th} the thermal velocity, σ_p the hole capture cross section, N_V the effective density of states of the valance band, and ω_{peak} the frequency of inflection in the C- ω -T spectra that is manifested as peaks in the $-\omega dC/d\omega$ versus ω spectra. These peaks shifted to higher frequency with the increase of temperature. The peak frequencies and their corresponding temperatures are used to construct an Arrhenius plot to determine the activation energy of the transition. The derivatives of capacitance spectra of D1, i.e., $-\omega dC/d\omega$ versus ω , of an n-i-p PSC with a 100-nm spiro-OMETAD HTL are plotted in Figure 2A,

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which are very similar to those measured from the perovskite-free n-p device (FTO/ SnO₂/C₆₀-SAM/spiro-OMeTAD/Au) (device 5) shown in Figure 2B. Both – ω dC/d ω versus ω spectra show fully developed and clear peaks, from which the Arrhenius plots were calculated (Figure 2C). The calculated activation energies of D1 (E_a(D1)) using corresponding Arrhenius plots are 0.166 \pm 0.005 and 0.172 \pm 0.005 eV, for the n-i-p and n-p devices, respectively. These activation energies are related to the electrical transport properties of the spiro-OMeTAD layer. The Arrhenius plot shown in Figure 2C also gives an attempt-to-escape frequency (v₀) of ~1.35 × 10⁹ Hz, which is calculated from the intercept with ln(ω_{peak}/T^2) of the linear fit of the plot at 300 K. We fabricated n-i-p solar cells with different perovskite thicknesses. We found that the D1 feature (activation energy) does not depend on the thickness of the perovskite layer.

Bias-dependent TAS measurements were performed for a PSC with 100-nm spiro-OMeTAD HTL to confirm the nature of D1 signature. In this case, various DC biases and constant AC modulation were applied. When the DC and AC modulations are applied together, the Fermi level (E_F) oscillates around a mean position. The band bending in spiro-OMeTAD HTL will depend on the DC bias. As a result, the measured activation energy of interface states will respond to the DC bias depending on its origin. As shown in Figure 2D, the activation energy related to D1 signature does not depend on the DC bias, confirming that D1 is not related to interface states but only related to trapping and de-trapping of charge carriers in the spiro-OMeTAD layer. Therefore, our results confirm that the D1 signature observed in perovskite PSCs is attributed to the spiro-OMeTAD HTL. The C-V measurement was conducted at 300 K and 6.28 \times 10⁴ Hz for the n-p device with 100-nm-doped spiro-OMeTAD HTL and without perovskite layer to gain more information of the electrical properties of the doped spiro-OMeTAD HTL. The Mott-Schottky plot, i.e., $1/C^2$ versus V_{DC}, measured for this n-p device at 300 K is shown in Figure S3A, which yields a carrier concentration of 5.86 \times 10¹⁷ cm⁻³, consistent with the value (1.2 \times 10¹⁷ cm⁻³) obtained from Hall measurement (300 K). The carrier concentration is calculated from the slope of the linear fit (blue line, Figure S3A) of the Mott-Schottky plot using the equation $N_{CV} = - (2/q\epsilon\epsilon_0)(dC^{-2}(V_{DC})/dV_{DC})^{-1}$. A built-in potential (V_{bi}) of 0. 59 V is determined from the line intercept with the bias voltage axis and a depletion width of 14.8 nm at a zero DC bias is calculated by $W = [2\epsilon\epsilon_0 (V_{bi} - V_{DC})/(qN_{CV})]^{\frac{1}{2}}$. The defect distributions, shown in Figure S3B, were calculated to understand the defect density of states (N_t) using the equation⁴⁷ $N_t = (V_{bi}/qW)(1/k_BT)[-\omega(dC/d\omega)]$. The energy of the defect distribution (E_t) is calculated by the equation⁴⁷ $E_t = E_a - k_B T \ln(2\nu_0/\omega)$. The distribution of defect density of state shows a peak value of \sim 9.88 × 10¹⁸ cm⁻³·eV⁻¹ located at ~0.16 eV with a corresponding integrated trap density of ~5.79 \times 10^{17} cm $^{-3}.$

Inverted p-i-n Devices

The D1 capacitance signature has also been observed in C- ω -T spectra measured from p-i-n PSCs. We fabricated p-i-n PSCs using PEDOT:PSS and PTAA HTLs with a cell structure of ITO/HTL/MA_{0.7}FA_{0.3}PbI₃/BCP/C₆₀/Ag. The perovskite layers were synthesized using identical processing conditions as the n-i-p PSCs discussed previously. The PE-DOT:PSS layer has a typical thickness of 70 nm, whereas the thickness is about 15 nm for polytriarylamines (PTAA). Figures 3A and 3B show the C- ω -T spectra measured from a p-i-n PSC (device 12) and a p-n device (device 13) using PEDOT:PSS HTLs. Their corresponding $-\omega$ dC/d ω versus ω spectra are shown in Figures 3C and 3D, respectively. The high-frequency capacitance D1 signature is clearly observed. The $-\omega$ dC/d ω versus ω spectra also show nice and clear peaks. However, when the PEDOT:PSS HTL is absent, the D1 capacitance signature is not observed (Figure S4), revealing that the D1 signature

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Figure 3. D2 Capacitance Signature

(A and B) C- ω -T spectra measured from (A) a p-i-n PSC (device 12) and (B) p-n device without the perovskite layer (device 13).

(C and D) Corresponding $-\omega dC/d\omega$ versus ω spectra of Figures 3A and 3B, respectively. The inserts in Figures 3B and 3D are enlarged spectra to show more details. The measurements were conducted in the dark with a 0.0 V DC bias.

also arises from the HTL. The characteristic frequencies shown in Figures 3C and 3D allow the extraction of the Arrhenius plots (Figure S5) from which the calculated activation energy $E_a(D1)$ is 0.019 \pm 0.001 eV for the p-i-n PSC and 0.016 \pm 0.001 eV for the p-n device, which are almost the same. These results further confirm that the D1 signature is attributed to the PEDOT:PSS HTL. It is noted that $E_a(D1)$ of PEDOT:PSS is about one order of magnitude smaller than that of spiro-OMeTAD (0.172 eV). This is because the conductivity of PEDOT:PSS is significantly higher than spiro-OMeTAD. We also measured the C- ω -T spectra of a p-i-n PSC using PTAA HTL (device 14) (Figure S6A). However, the $-\omega dC/d\omega$ versus ω spectra do not show D1 peaks at high frequency ($\omega < 10^5$ Hz), indicating no D1 capacitance signature (Figure S6B). The results are consistent with that of the perovskite-free device (PTAA/C₆₀/BCP) as shown in Figure S6C. In p-i-n PSCs using PTAA HTLs, the PTAA must be ultra-thin because of its low electrical conductivity. Therefore, the PTAA layer is expected to be fully depleted. As a result, there is no trapping and de-trapping process during AC perturbation, explaining why no D1 capacitance signature is observed in p-i-n PSCs using ultra-thin PTAA HTLs.

We also measured C- ω -T spectra from HTL-free devices using different perovskites including MAPbI₃, FA_{0.3}MA_{0.7}PbI₃, and perovskites with different thicknesses. As shown in Figure S7, all devices showed no D1 signature, suggesting that the "apparent insulating behavior" is a general phenomenon for all halide perovskites.

The Dielectric Constant

As discussed previously, in PSCs without HTL, the high-frequency capacitance is due to a contribution from the entire perovskite layer and can be considered as C_g , implying that the dielectric constant can be calculated using the high-frequency capacitance. However, to obtain reliable values, the capacitance contribution from CTLs must be excluded. We measured C-V-T curves of a device with the structure of an FTO/SnO₂/C₆₀-SAM/MA_{0.7}FA_{0.3}PbI₃/Au device (device 4) at an AC frequency of 6.28 × 10⁴ Hz. As expected, the capacitance does not significantly vary with temperature or bias voltage (Figure S8A). As a result, a consistent dielectric constant of

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(A and B) Dielectric constant as a function of temperature of (A) PSCs with different architectures: $FTO/SnO_2/C_{60}$ -SAM/MA_{0.7}FA_{0.3}PbI₃/spiro-OMeTAD/Au with 100 nm (device 1) (circle markers), 0 nm (device 7) (diamond markers) HTLs, and without ETL/HTL, FTO/MA_{0.7}FA_{0.3}PbI₃/Au (device 8) (diamond markers), and (B) the n-p junction without a perovskite layer.

These results are calculated from the capacitance voltage measurements performed in the dark at 6.28×10^4 Hz angular frequency (Figure S8).

 \sim 33 is obtained for all temperatures (Figure 4A). Similar dielectric constants were observed when the thickness of the perovskite layer was varied (Figure S9A). For the device without ETL and HTL, a consistent dielectric constant of \sim 30 is also obtained for all temperatures (Figure 4A). The result indicates that the SnO_2 ETL has a rather small effect on the high-frequency capacitance. However, for PSCs with HTLs, the dielectric constant of the perovskite layer cannot be reliably calculated due to the capacitance contribution from the HTL (see Figures 4A, S8C, and S8D). All organic HTL materials such as spiro-OMeTAD, PTAA, and PEDOT:PSS are ionic conductors whose electrical conductivity sensitively depends on the temperature and frequency. At low enough temperature, thick HTLs become insulating, which contributes to the measurement of total capacitance. Therefore, the calculated dielectric constant is expected to be much lower at low temperatures. Indeed, we observed this trend for dielectric constants calculated from the C-V-T results measured from a PSC with a 100-nm-thick spiro-OMeTAD HTL (device 1). A similar dielectric constant-temperature dependency trend was observed in an n-p device, i.e., FTO/SnO₂/C₆₀-SAM/spiro-OMeTAD/Au (device 5), as shown in Figure 4B. The small discrepancy is likely due to the influence of low electrical conductivity of the perovskite layer in the n-i-p PSC. Our results suggest that to reliably measure the dielectric constant of perovskite layers in PSCs, the contribution of CTL materials must be carefully excluded, which is consistent with previous suggestions by Lopez-Varo et al.⁴⁸ and Almora et al.⁴¹ The discrepancies of dielectric constants reported in the literature (Table S1) are due to the influence of CTL materials, rather than the different properties of perovskite or different phases as reported in literature. We have also measured the dielectric constants of HTL-free device using MAPbl₃ perovskite layer with three thicknesses at various temperatures. As shown in Figure S9B, dielectric constant steps or discontinuities near 160 K are observed, which is consistent with previous reports.^{49,50} Such discontinuities are due to phase transition of MAPbI₃ perovskite.⁵¹ However, FAPbI₃ does not show a phase transition at a similar temperature.⁵¹ Therefore, no dielectric constant discontinuity was observed in MA_{0.7}FA_{0.3}PbI₃ PSCs.

High-Temperature and Low-Frequency Capacitance Signature (D2)

Planar n-i-p Devices

D2 refers to the capacitance signature seen at low frequencies ($\omega < 10^3$ Hz) and at higher temperatures between \sim 240 and 300 K with capacitance varying between

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 10^{-7} and $<10^{-5}$ F/cm² (Figure 1A). D2 capacitance signature is observed in the devices with and without HTLs. Similar feature has been reported previously.^{8,12} Comparison of TAS spectra shown in Figure 1 suggests that the SnO₂ ETL and spiro-OMeTAD HTL also contribute to the D2 signature capacitance. It is noted that the low-frequency capacitance spectra measured from n-i-p PSCs (devices 1-3) show no signature, which is seen more clearly if the spectra are plotted in a linear scale (Figure S10A). The $-\omega dC/d\omega$ versus ω spectra from the TAS spectra of the n-i-p PSC using 100-nm-thick spiro-OMeTAD HTL (device 1) are shown in Figure S10B. The plot shows no clear peaks, which indicates dispersive capacitance signatures. Therefore, it is inappropriate to consider these low-frequency spectra as a single trapping and de-trapping signature. It is noted that in literature the $E_a(D2)$ reported for n-i-p PSCs with spiro-OMeTAD HTL were not calculated based on the Arrhenius plots extracted from $-\omega dC/d\omega$ versus ω spectra due to the lack of clear peaks. Instead, the dln(C)/dln(ω) versus ω spectra were used to extract the Arrhenius plot since the dln(C)/dln(ω) versus ω spectra show clear peaks. Following this approach, $dln(C)/dln(\omega)$ versus ω spectra of the n-i-p PSC (device 1) with its C- ω -T spectra shown in Figure 1A and the n-i device (device 4) with its C-ω-T spectra shown in Figure 1B are shown by the red and blue spectra in Figure S11A, respectively. These spectra show clear peaks at low frequencies, allowing extractions of Arrhenius plots shown Figure S11B, from which the calculated $E_a(D2)$ is 0. 592 \pm 0.012 eV for the n-ip PSC but only 0.399 \pm 0.001 eV for the n-i device without spiro-OMeTAD HTL. Because the perovskite layers were synthesized using identical precursors and conditions, such large discrepancy suggests that it is inappropriate to use $dln(C)/dln(\omega)$ versus ω spectra to calculate activation energy. We attributed this to the contribution of capacitance at low frequencies from both spiro-OMeTAD HTL and SnO₂ ETL, which couple with the capacitance of perovskite. To verify the contribution of SnO_2 ETL, we measured the C- ω -T spectra (Figure S12) from a p-n device (ITO/ spiro-OMeTAD/C₆₀/BCP/Ag) (device 16). The comparison between Figures 1D and S12 shows clear contribution from SnO₂ ETL to the low-frequency capacitance.

Inverted p-i-n Devices

It is interesting to note that in the p-i-n PSCs, the ETLs and HTLs do not make any observable coupling to the low-frequency capacitance, which is clearly seen in Figures 3 and S6. As seen in Figure 3C, the $-\omega dC/d\omega$ versus ω low-frequency spectra show nice and clear peaks for a large temperature range (230-300 K). The Arrhenius plot obtained from these spectra is shown in Figure S5, which gives an $E_a(D2)$ of 0.363 ± 0.012 eV for the p-i-n PSC using PEDOT:PSS HTL (device 12). As seen in Figure 3D, the device with PEDOT:PSS and without perovskite layer (device 13) does not show D2 signature. Therefore, $E_a(D2)$ value obtained from the PSC using PE-DOT:PSS HTL has no contribution from the HTL. Similar results are obtained for pi-n PSCs using PTAA HTL (device 14) (Figure S6) and the device without HTL and non-SnO₂ ETL (device 16) (Figure S4). The corresponding calculated $E_a(D2)$ are 0.371 \pm 0.009 eV for the PSC using PTAA HTL (Figure S6D) and 0.365 \pm 0.022 eV for the PSC without HTL (Figure S4B). These values are very similar to that obtained from the p-i-n PSC using PEDOT:PSS HTL (device 12). Therefore, for inverted p-i-n PSCs, the E_a(D2) value can be used to characterize the electrical transport properties of the perovskite layer at low frequencies, which are related to ion migration. D2 is in fact due to the ionic relaxation⁵² of the perovskite absorber, which behaves as an ionic insulator at high frequencies and an ionic conductor at low frequencies with a demarcation point at $\omega_{ir} = \omega_{peak}$, where $\omega_{ir} = \sigma_{ion}/\epsilon_r \epsilon_0$, σ_{ion} is the ionic conductivity in the perovskite absorber. The ionic relaxation frequencies obtained from

Therefore, for PSCs in n-i-p cell structure, $E_a(D2)$ values are labeled by "-" in Table 1,

which means that D2 is unmeasurable using the conventional method.

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Figure 5. The Ionic Conductivity of Perovskite

(A) lonic conductivity as a function of temperature and (B) corresponding Arrhenius plot of an ITO/ PEDOT:PSS/MA_{0.7}FA_{0.3}Pbl₃/C₆₀/BCP/Ag PSC (device 12) calculated using ionic relaxation frequencies obtained from Figure 3C.

Figure 3C are 1.578 Hz (230 K), 2.701 Hz (240 K), 7.910 Hz (250K), 17.042 Hz (260 K), 34.003 Hz (270 K), 58.190 Hz (280 K), 107.530 Hz (290 K), and 157.830 Hz (300 K). We calculated the ionic conductivity, σ_{ion} , at various temperatures using these ionic relaxation frequencies and a dielectric constant ϵ_r of 30 (Figure 4). The results are shown in Figure 5A. The ionic conductivity increases with temperature, reaching 4.190 × 10⁻¹⁰ $\Omega^{-1} \cdot \text{cm}^{-1}$ at 300 K. The corresponding Arrhenius plot, $\ln(\sigma_{ion})$ versus 1/k_BT, is shown in Figure 5B. The calculated activation energy from this Arrhenius plot is 0.408 ± 0.012 eV, which is consistent with the activation energy (0.363 ± 0.012 eV) calculated from $\ln\omega_{\text{peak}}/\text{T}^2$ versus 1/k_BT Arrhenius plot (Figure S5).

Conclusions

In conclusion, we have shown that there is no trapping and de-trapping of charge carriers in perovskite layers in PSCs in TAS measurements, revealing an apparent insulating behavior of the perovskite layers. It implies that TAS cannot be applied to correctly analyze the properties of defects in perovskite absorbers or at interfaces in PSCs without careful scrutiny. The so-called D1 high-frequency capacitance signature is due to the CTLs used in PSCs. For PSCs without HTLs, the high-frequency capacitance can be considered geometric capacitance and can be used to estimate the dielectric constant of perovskite layers. The significant discrepancy of temperature-dependent dielectric constants previously reported in literature is due to the influence of HTLs. In n-i-p PSCs, so-called low-frequency capacitance signatures cannot be used to evaluate the electrical transport properties, due to coupling with ETLs and HTLs. However, in p-i-n PSCs, the organic ETLs and HTLs do not significantly influence the low-frequency capacitance. Therefore, the D2 capacitance signature can be used to characterize the ionic transport properties of the perovskite layer.

EXPERIMENTAL PROCEDURES

Full details of the device fabrication method can be found in the Supplemental Information.

Devices Characterizations

All temperature-dependent measurements were performed using a Solartron Modulab potentiostat equipped with a frequency response analyzer (Ametek Inc.). A built in XM-studio software to the Modulabl system is used to set up, run experiments, and obtain the results. The thermal admittance spectroscopy measurements were done under dark equilibrium and a constant AC modulation voltage of 30 mV_{rms} with frequency (f) swept from 10^6 to 10^{-1} Hz. In bias-dependence TAS measurements, a DC bias voltage was

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varied from -0.4 to 1.0 V at a step size of 0.1 V. Capacitance voltage measurements were performed in dark with DC bias voltage (V_{DC}) swept from -1.5 to 0.5 V with 30 mV_{rms} AC modulation voltage (V_{AC}) and $\omega = 2\pi \times 10^4$ Hz frequency. A liquid-nitrogen cooled cryo-system (Janis VPF-100 system) was used to carry out all the temperature dependent (from 120 to 310 K with a step size of 10 K) measurements. The temperature was measured and controlled by two channels temperature controller (Lakeshore 330). One sensor (Silicon diode thermometer) was connected to the thermal impedance displacer that is machined to fit precisely within the tail section of the liquid nitrogen dewar, and the other sensor was mounted on the top of the device directly to ensure that the recorded temperature is the device temperature. Heater and thermometer are provided on the thermal impedance displacer to better control the sample stage temperature. In order to ensure the temperature is stable, between one temperature step to another, the measurements were delayed for 15 min. LABVIEW software was used for controlling the temperature and running the experiment by Modulab.

SUPPLEMENTAL INFORMATION

Supplemental Information can be found online at https://doi.org/10.1016/j.joule. 2020.01.012.

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AUTHOR CONTRIBUTIONS

R.A.A. carried out the measurements and data analysis. Z.S., C.C., C.L., C.W., and L.C. fabricated devices in this study. M.A.R. and R.J.E. assisted in the low-temperature capacitance measurements. X.W. provided necessary theoretical information. R.A.A., J.V.L., and Y.Y. wrote the manuscript. All co-authors commented on the manuscript. Y.Y. directed and supervised the project leading to this publication.

DECLARATION OF INTERESTS

The authors declare no competing interests.

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